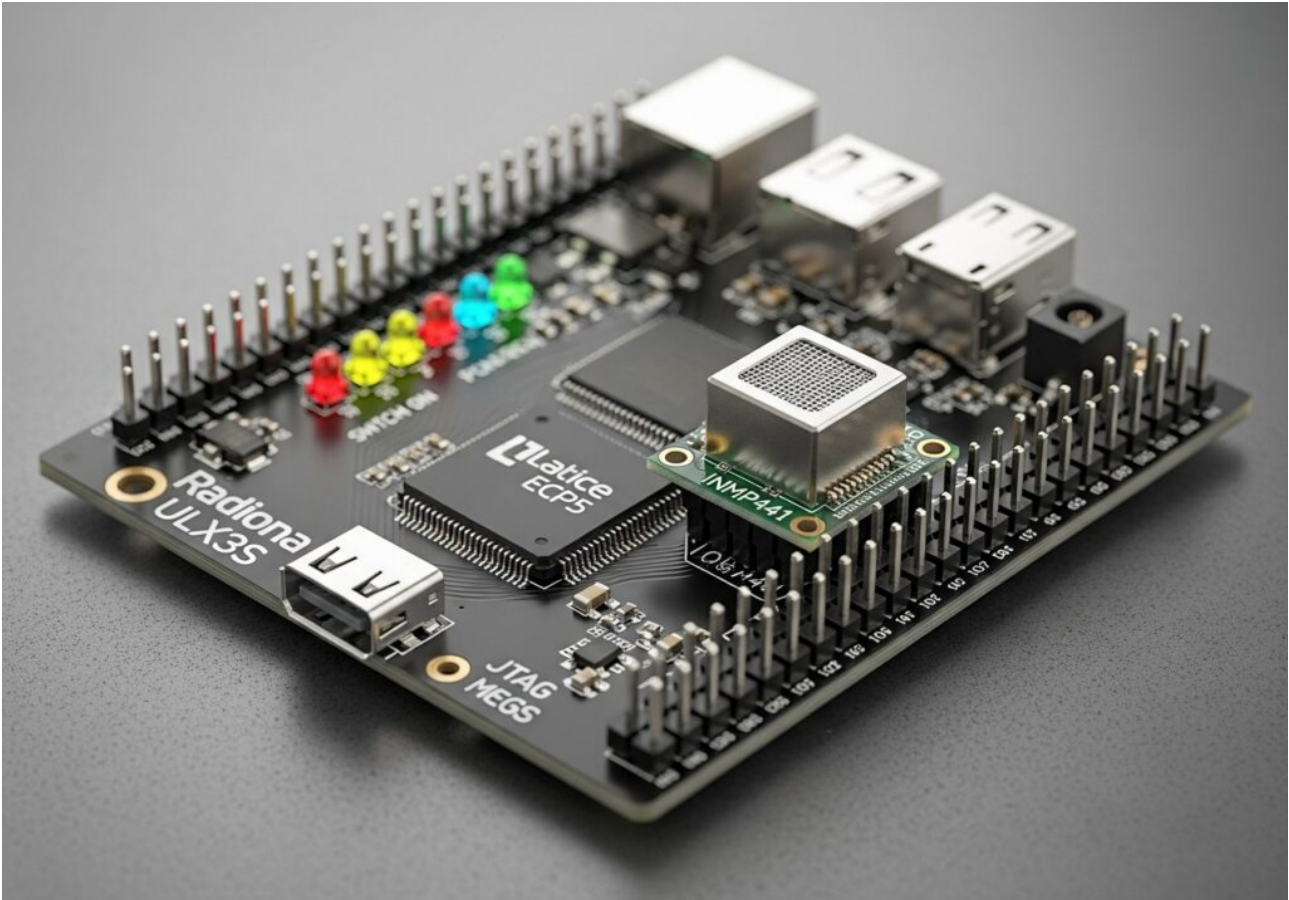
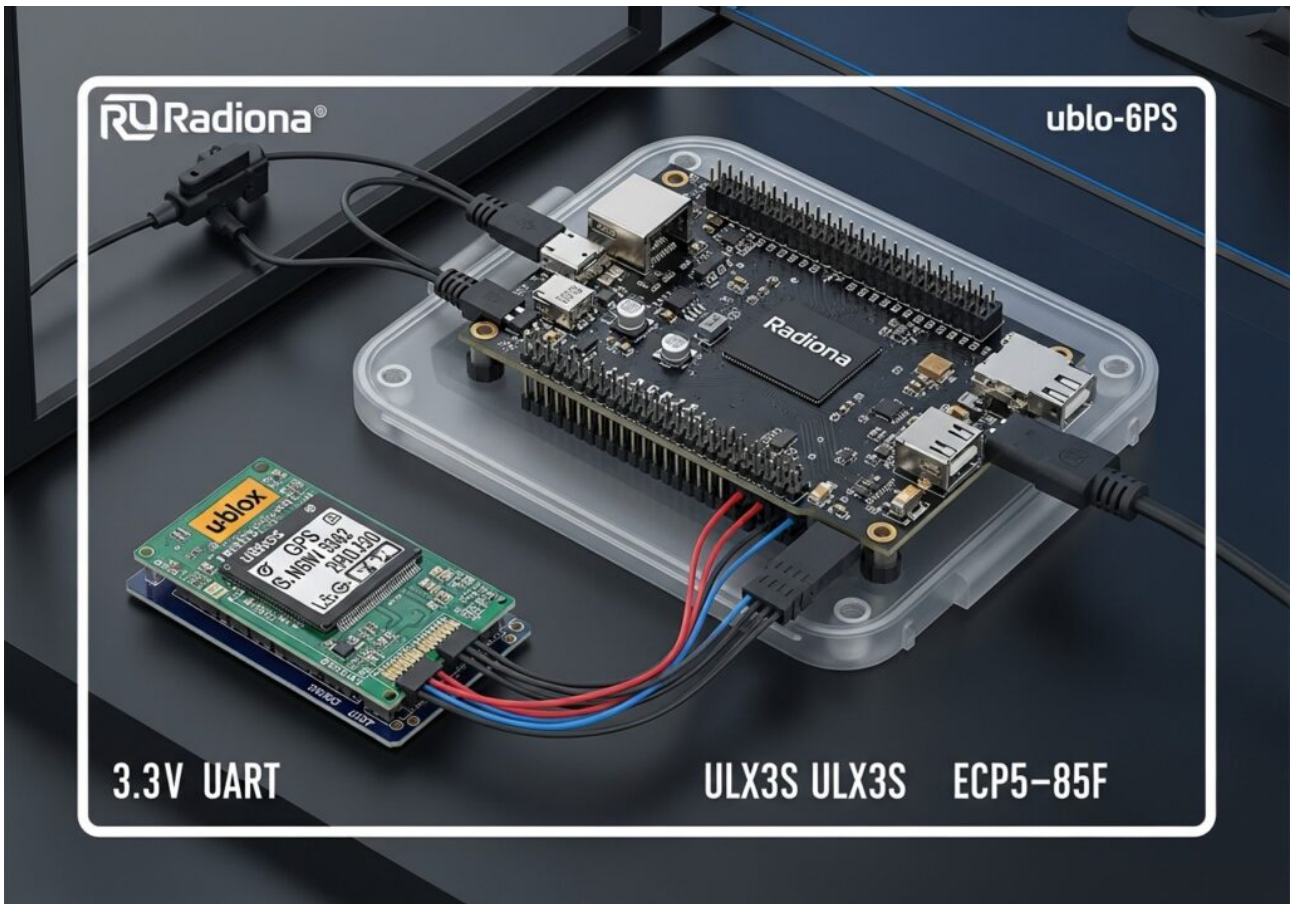


Practical case: voice-controlled RUN/STOP on ULX3S



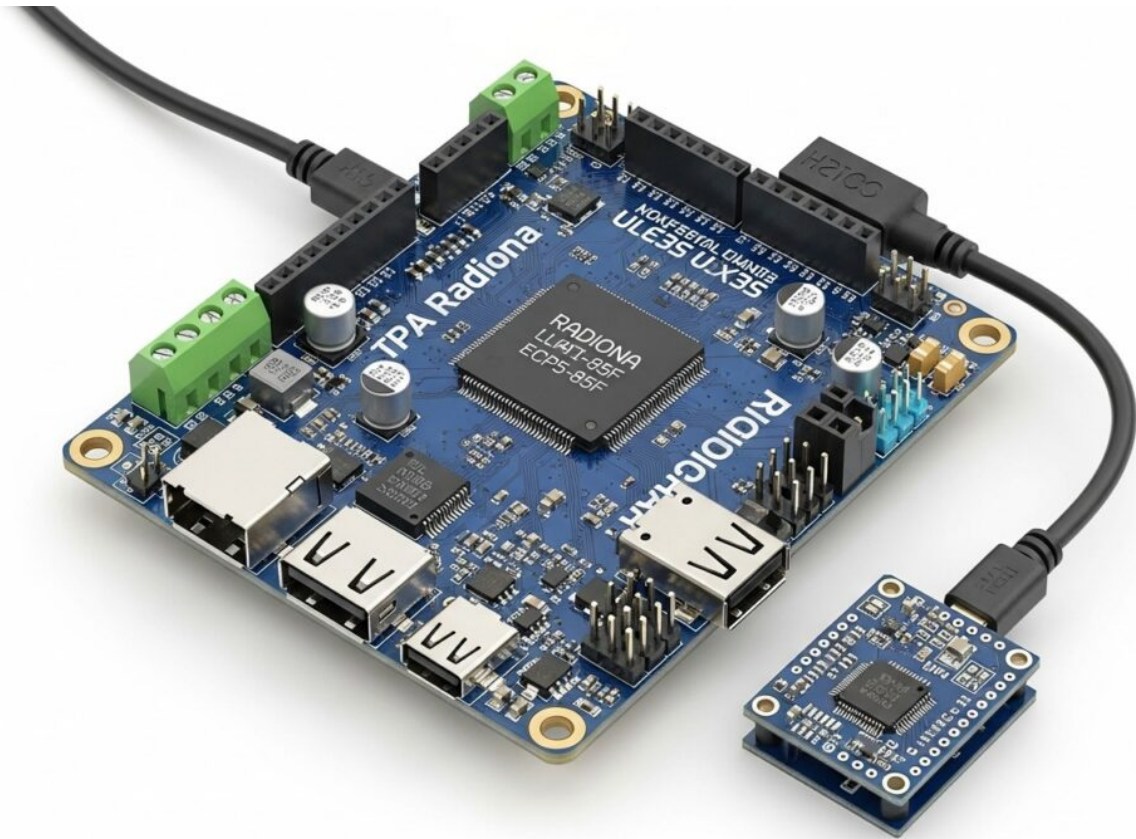
A compact FPGA voice-activity burst detector on a Radiona ULX3S Lattice ECP5-85F using an INMP441 I2S MEMS microphone. A short, loud spoken burst such as...

Practical case: NMEA GPS Monitor on ULX3S



A practical FPGA-based GPS monitor using the Radiona ULX3S Lattice ECP5-85F , a u-blox NEO-6M GPS module, and 3.3 V UART wiring. It will receive NMEA data at...

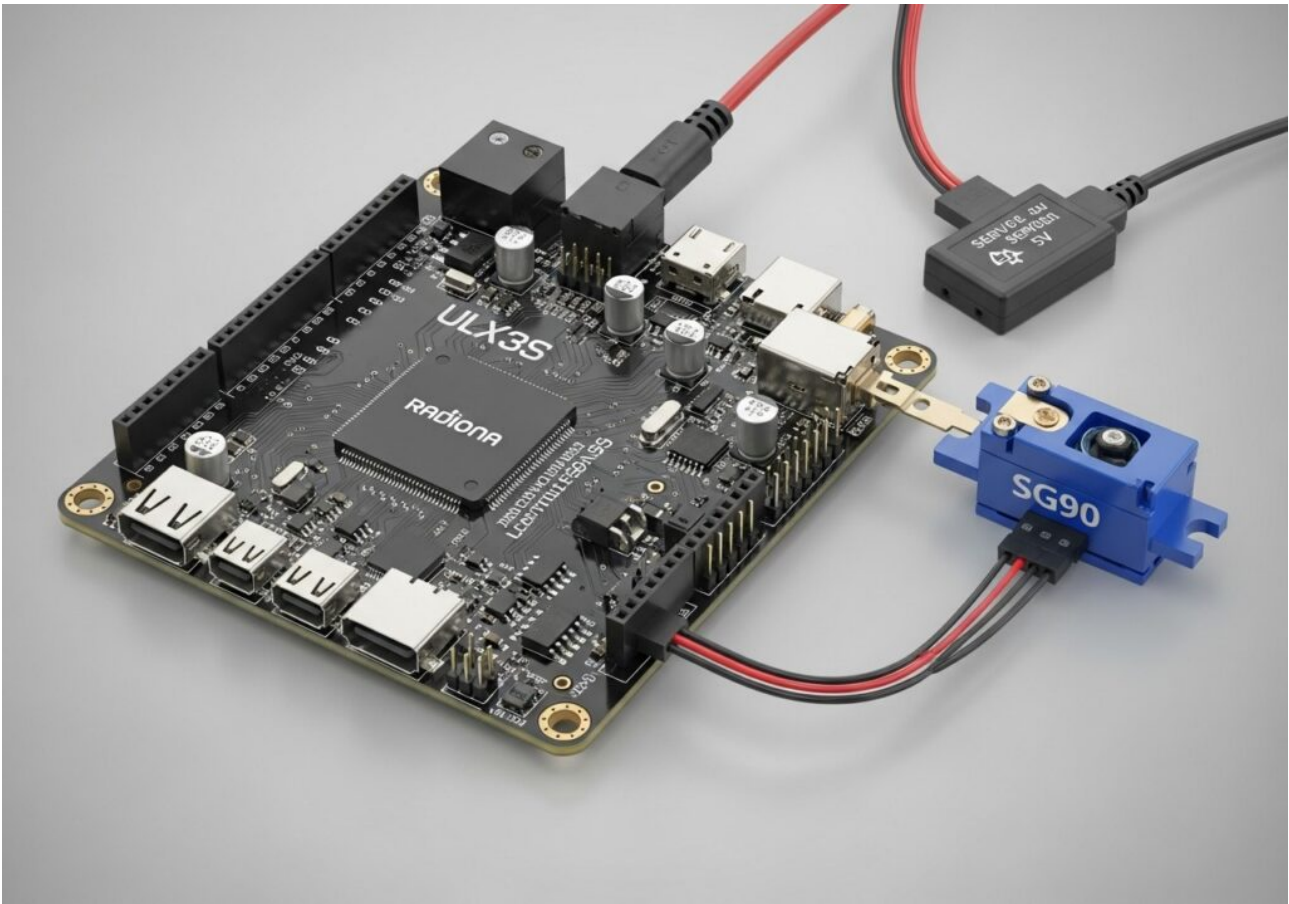
Practical case: passive UART monitor with ULX3S



Objective and use case

What you'll build: A practical UART monitor on the Radiona ULX3S (Lattice ECP5-85F) that passively taps a real 3.3 V, 115200 baud,...

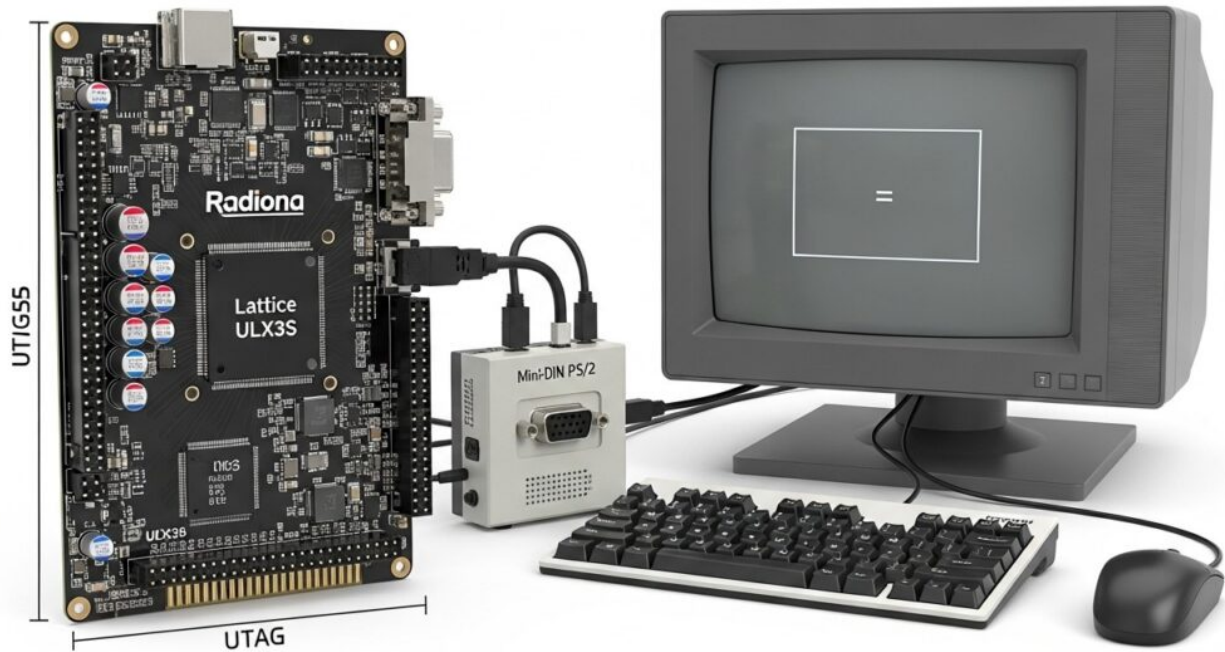
Practical case: SG90 servo tester on ULX3S



Objective and use case

What you'll build: A bench servo tester on the Radiona ULX3S (Lattice ECP5-85F) that generates a hobby-servo PWM control signal for...

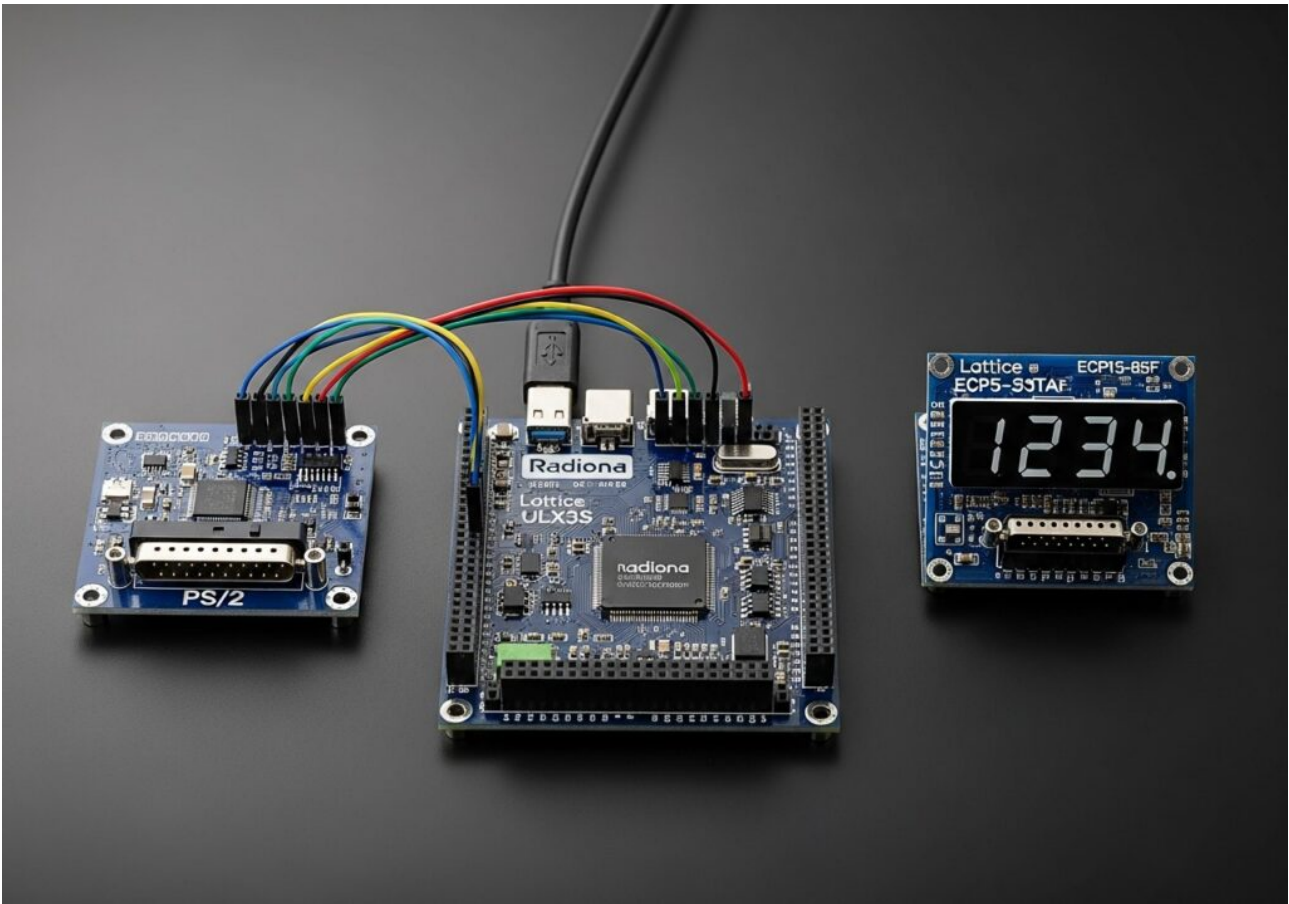
Practical case: PS/2 to VGA monitor with ULX3S



Objective and use case

What you'll build: A PS/2-to-VGA keystroke monitor on the Radiona ULX3S (Lattice ECP5-85F) that captures keyboard bytes in real time...

Practical case: FPGA PIN lock with Radiona ULX3S Lattice ECP5-85F



Objective and use case

What you'll build: A compact FPGA PIN-lock prototype on the Radiona ULX3S (Lattice ECP5-85F) that reads a 4-digit code from a PS/2...