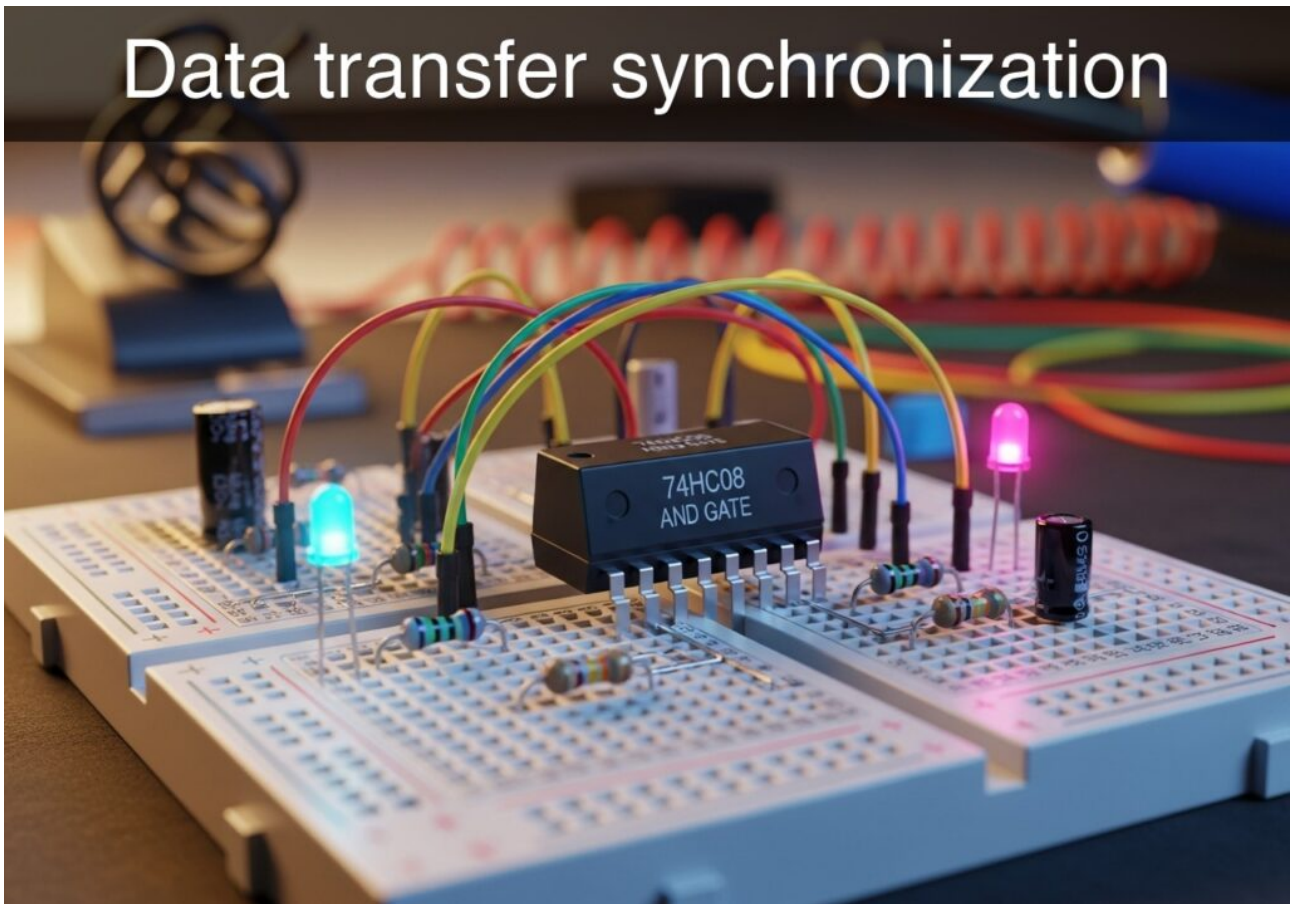


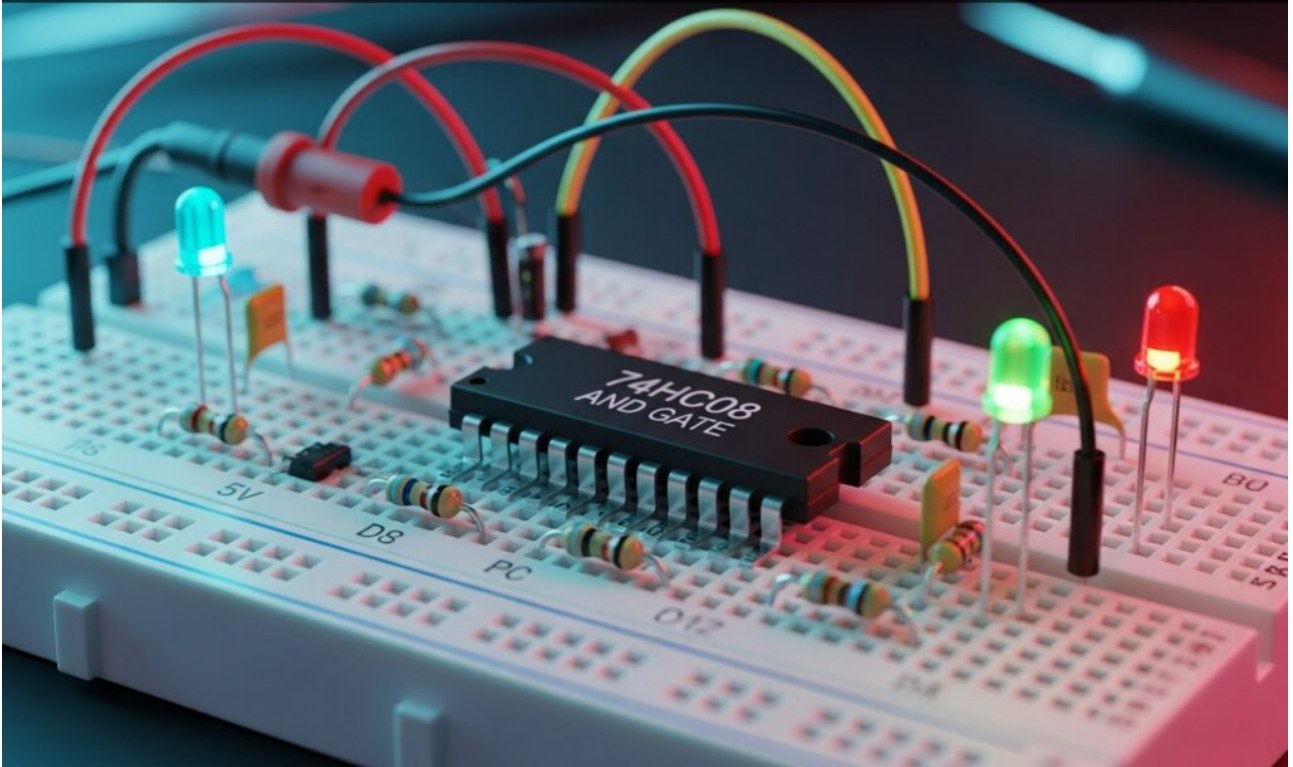
Practical case: Data transfer synchronization



Master Digital Electronics by building a clock gating circuit with an AND gate. Ensure precise synchronization pulses and reduce power only when data is ready.

Practical case: Safety interlock in a chemical reactor

Safety interlock in a chemical reactor



Master Digital Electronics by building a redundant safety interlock. Use an AND gate circuit to validate three critical inputs and activate a high-power relay.