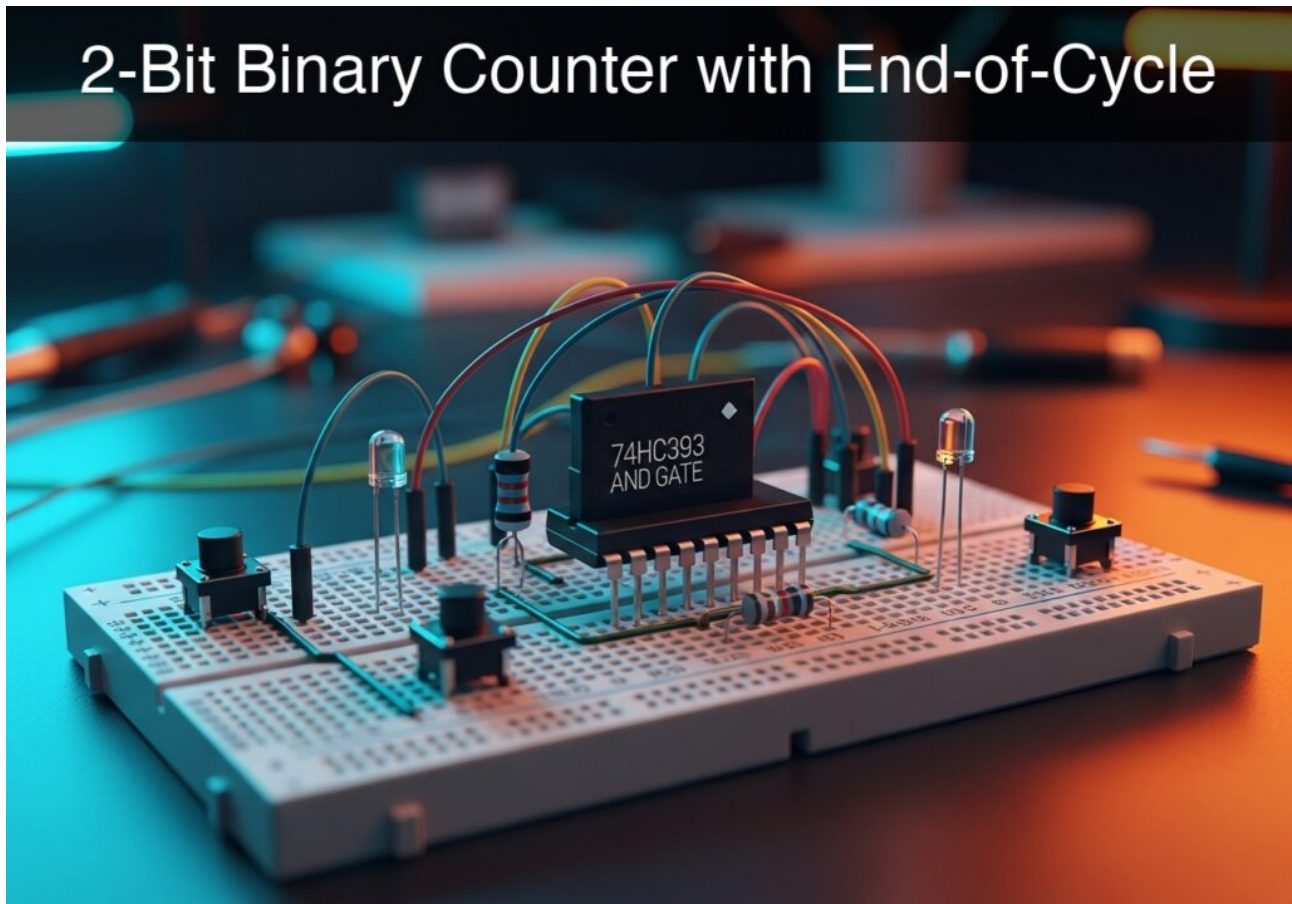


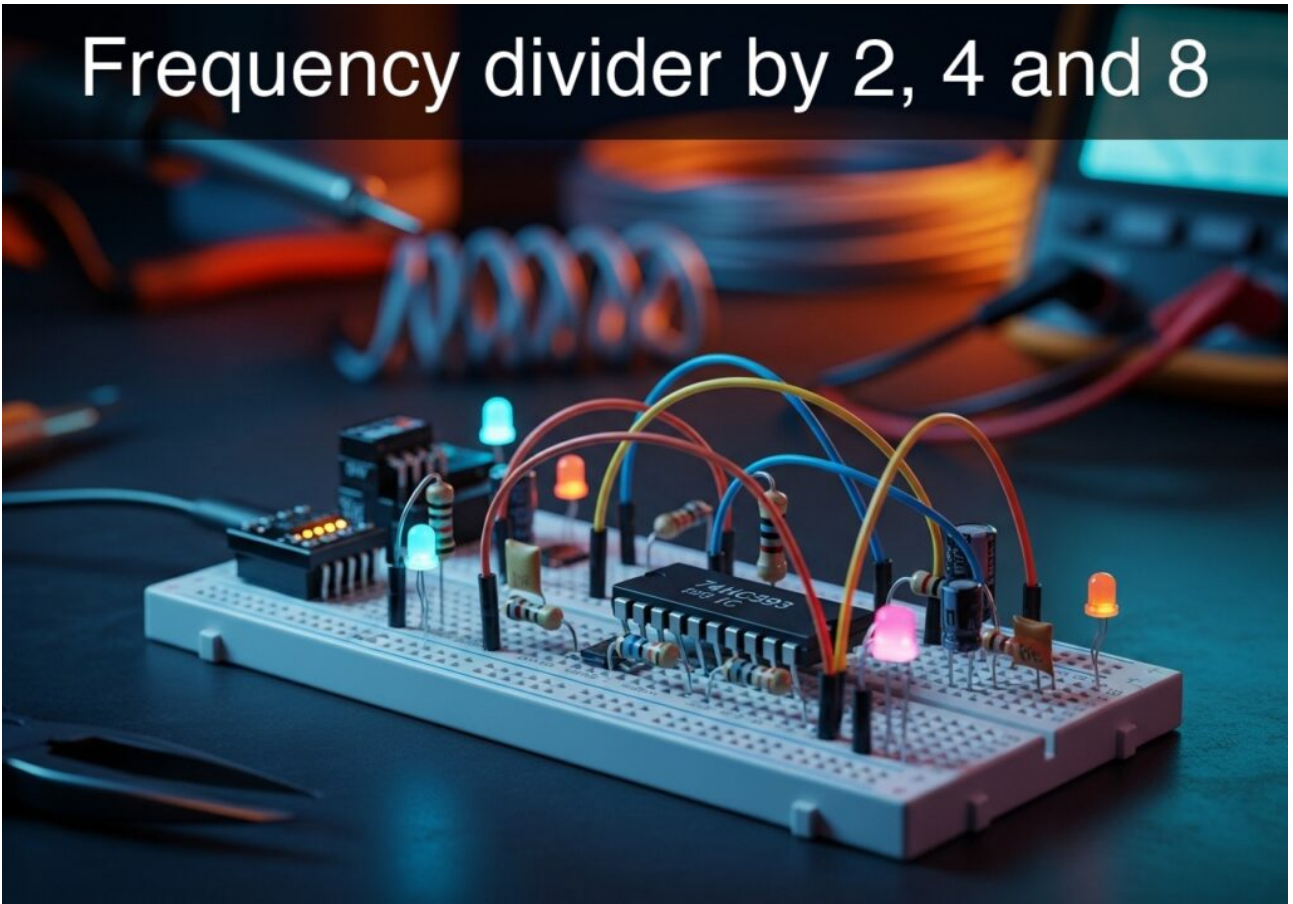
Practical case: 2-Bit Binary Counter with End-of-Cycle



Level: Basic – Build a 0 to 3 binary counter with manual clock, reset, and a maximum count indicator.

Practical case: Frequency divider by 2, 4 and 8

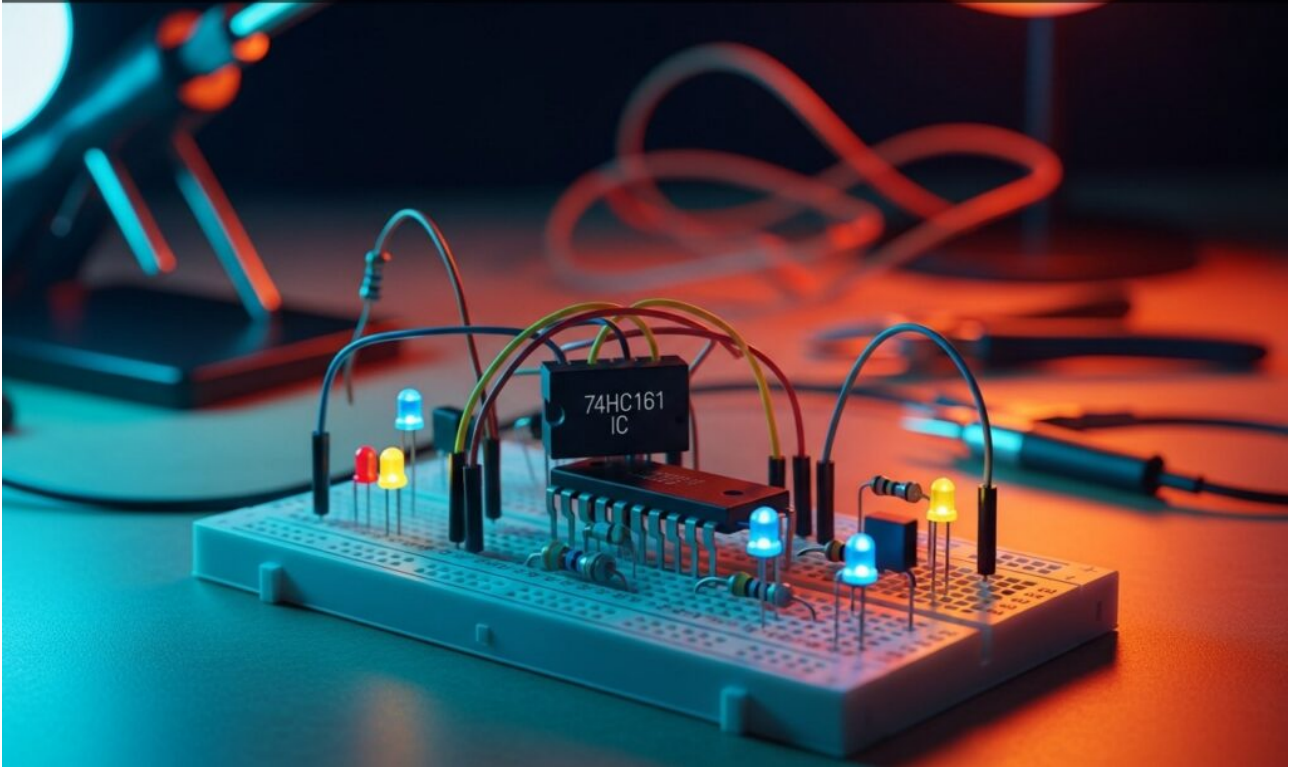
Frequency divider by 2, 4 and 8



In this Digital Electronics lab, use a Binary counter to build a frequency divider. Verify square wave outputs at $f/2$, $f/4$, and $f/8$ relative to the clock.

Practical case: 4-bit up counter with LEDs

4-bit up counter with LEDs



Build a practical Digital Electronics circuit with a Binary counter. Visualize the 0 to 15 sequence on LEDs and verify synchronous clock signals.